Anode Shorted Gate Turn-Off Thyristor Type SA45AP1000TB



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-	Voltage Code	Outline Code	Current code	Type code	Special code	Optional code	

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Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{DRM}	Repetitive peak off-state voltage, (note 1)	4500	V
V_{RSM}	Non-repetitive peak off-state voltage, (note 1)	4500	V
V _{DC-link}	Maximum continues DC-link voltage	2800	V
V_{RRM}	Repetitive peak reverse voltage	18	V
V _{RSM}	Non-repetitive peak reverse voltage	18	V
note 1)	$V_{GK} = -2V$		

	OTHER RATINGS	MAXIMUM LIMITS	UNITS			
I_{TGQ}	Peak turn-off current (note 1)	1000	А			
L _S	Snubber loop impedance, $I_{TM} = I_{TGQ}$ (note 1)	300	nH			
$I_{T(AV)M}$	Mean on-state current, T _{sink} = 55°C, (note 2)	443	А			
I _{T(RMS)}	Nominal RMS on-state current, T _{sink} = 25°C (note 2)	867	А			
I _{TSM}	Peak non-repetitive surge current t _p = 10ms (note 3)	6.5	kA			
I _{TSM2}	Peak non-repetitive surge current t _p = 2ms (note 3)	11.45	kA			
l ² t	I^2 t capacity for fusing $t_p = 10$ ms	$211.25 \cdot 10^3$	A^2s			
(di/dt) _{cr}	Critical rate of rise of on-state current, (note 4)	300	A/µs			
P_{FGM}	Peak forward gate power	185	W			
P_{RGM}	Peark reverse gate power	7	kW			
I _{FGM}	Peak forward gate current	100	А			
V_{RGM}	Peak reverse gate voltage (note 5)	18	V			
t _{off}	Minimum permissible off-time (note 1)	80	μs			
t _{on}	Maximum permissible on-time	20	μs			
T _{jop}	Operating temperature range	-40 to +125	°C			
T _{stg}	Storage temperature range	-40 to +125	°C			
note 1)	T_j = 125°C, V_D = 2800V, V_{DM} \leq 4500V, di_{GQ}/dt = 25A/ μ s, I_{TGQ} = 1000A and C_S = 1 μ F					
note 2)	Double-side cooled, single phase, 50Hz, 180° half-sinewave.					
note 3)	$T_{j(initial)}$ = 125°C, single phase, 50Hz, 180° sinewave, re-applied voltage V_D = $V_R \le 10V$					
note 4)	For di/dt > 300A/µs please consult factory.					
note 5)	May exceed this value during turn-off avalanche period.					



Characteristics

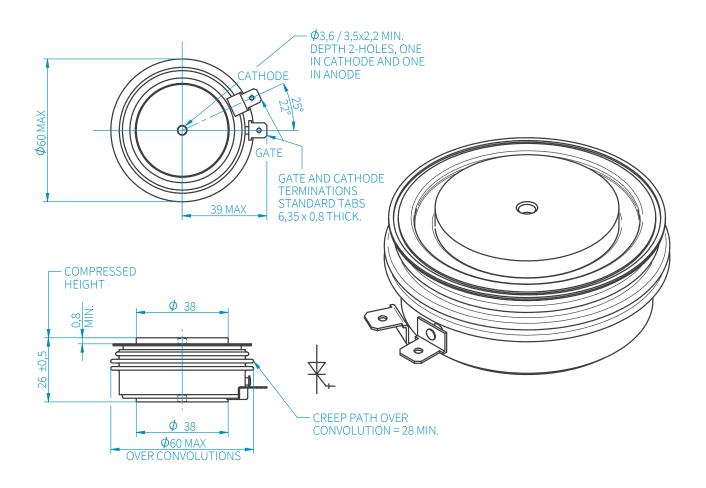
	PARAMETER	TEST CONDITIONS	MIN	ТҮР	MAX	UNITS
V_{TM}	Maximum peak on-state voltage	I _G = 2A, I _T = 1000A	-	3.65	4.0	V
IL	Latching current	T _j = 25°C	-	10	-	А
I _H	Holding current	T _j = 25°C	-	10	-	А
$(dv/dt)_{cr}$	Critical rate of rise of off-state voltage	$V_{D} = 2800V_{DRM}, V_{GR} = -2V$	1000	-	-	V/µs
I _{DRM}	Peak off-state current	Rated V_{DRM} , $V_{GR} = -2V$	-	-	50	mA
I _{RRM}	Peak reverse current	V _{RR} = 16V	-	-	60	mA
I_{GKM}	Peak negative gate leakage current	$V_{GR} = -16V$	-	-	60	mA
	Gate trigger voltage	$T_j = -40$ °C, $V_D = 25$ V, $R_L = 25$ m Ω	-	1.0	-	V
V_{GT}		$T_j = 25$ °C, $V_D = 25$ V, $R_L = 25$ m Ω	-	0.8	1.0	V
		$T_j = 125$ °C, $V_D = 25V$, $R_L = 25m\Omega$	-	0.6	-	V
	Gate trigger current	$T_j = -40$ °C, $V_D = 25V$, $R_L = 25m\Omega$	-	1.8	3.5	А
I_{GT}		$T_j = 25$ °C, $V_D = 25$ V, $R_L = 25$ m Ω	-	0.75	1.5	А
		$T_j = 125$ °C, $V_D = 25V$, $R_L = 25m\Omega$	-	0.2	0.4	А
t _d	Delay time	V _D = 2800V, I _{TGQ} = 1000A, di _T /dt = 300A/μs, I _{GM} = 20A,	-	0.9	-	μs
t _{gt}	Turn-on time	$d_{if}/dt = 300A/\mu s$, $i_{GM} = 20A$, $d_{ig}/dt = 20A/\mu s$	-	3.4	6.0	μs
t _f	Fall time		-	1.25	-	μs
t _{gq}	Turn-off time		-	14	16	μs
I_{CQM}	Peak turn-off gate current	$V_D = 2800V, V_{TGQ} = 1000V,$	-	300	-	А
Q_{GQ}	Turn-off gate charge	$di_{GQ}/dt = 25A/\mu s$, $V_{GR} = -16V$, $C_S = 1\mu F$	-	2.2	3.0	mC
t _{tail}	Tail time		-	13	20	μs
t _{gw}	Gate off-time (note 3)		100	-	-	μs
	Thermal resistance, junction to sink	Double side cooled	-	-	38	K/kW
R_{thJK}		Cathode side cooled	-	-	61	K/kW
		Anode side cooled	-	-	101	K/kW
F	Mounting force	(note 2)	13	-	17	kN
W _t	Weight		-	325	-	g
note 1)	Unless otherwise indicated T _j = 125°C					
note 2)	For other clamping forces, consult factory.					
note 3)	t_{g_W} is the period during which the gate circuit is required to remain at low impedance to allow for the passage of t_{tail} .					

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